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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: John de Q. Walker, et al. ) Group Art Unit: 2811  
Serial No.: 10/035,346 ) ) Examiner: Ori Nadav  
Filed: December 28, 2001 ) ) Atty. Docket No.: 01-152  
For: CMOS varactor with constant dC/dV characteristic ) )

FAX COPY RECEIVED

AUG 29 2002

TECHNOLOGY CENTER 2800

**RESPONSE TO OFFICIAL ACTION**  
**Restriction/Election Requirement**

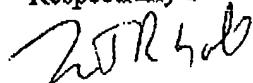
Hon. Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

This response is presented to the Office Action mailed August 20, 2002, wherein the Examiner required restriction pursuant to 35 U.S.C. §121. Election is hereby made, without traverse, to prosecute Group I, claims 1-10.

Please withdraw claims 11- 17 (i.e., Group II) from consideration.

Respectfully Submitted,



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Milpitas, CA 95035-7451

Date: August 28, 2002